

FIG. 1

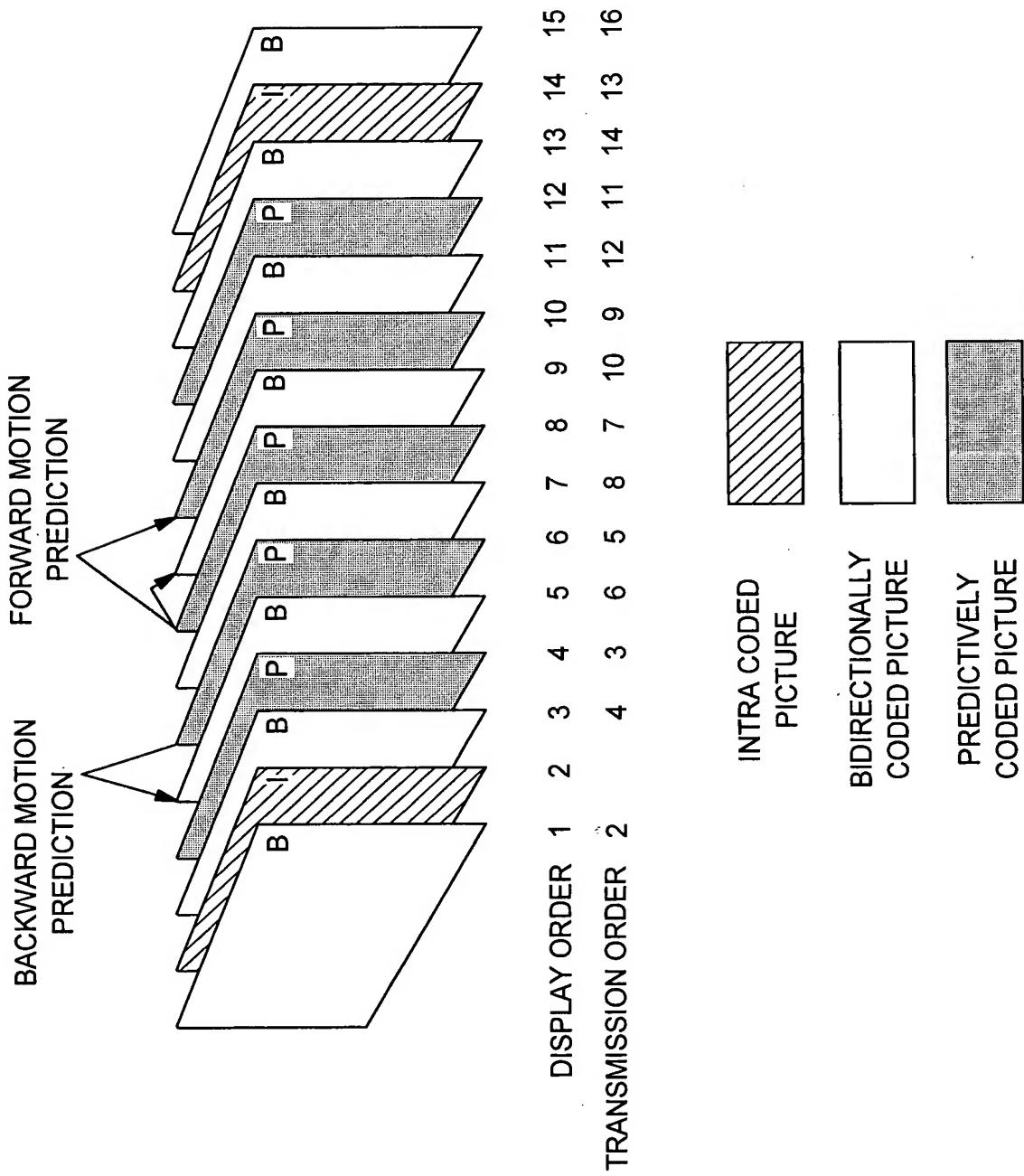


FIG. 2

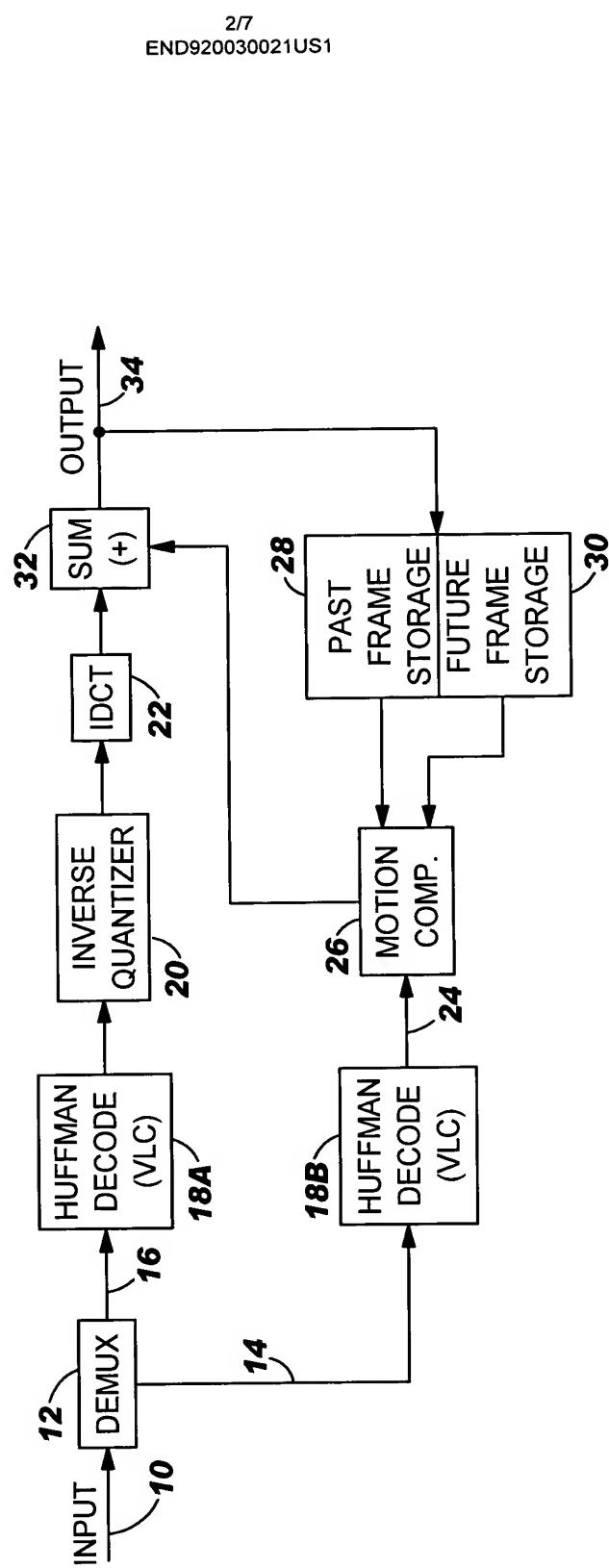


FIG. 3

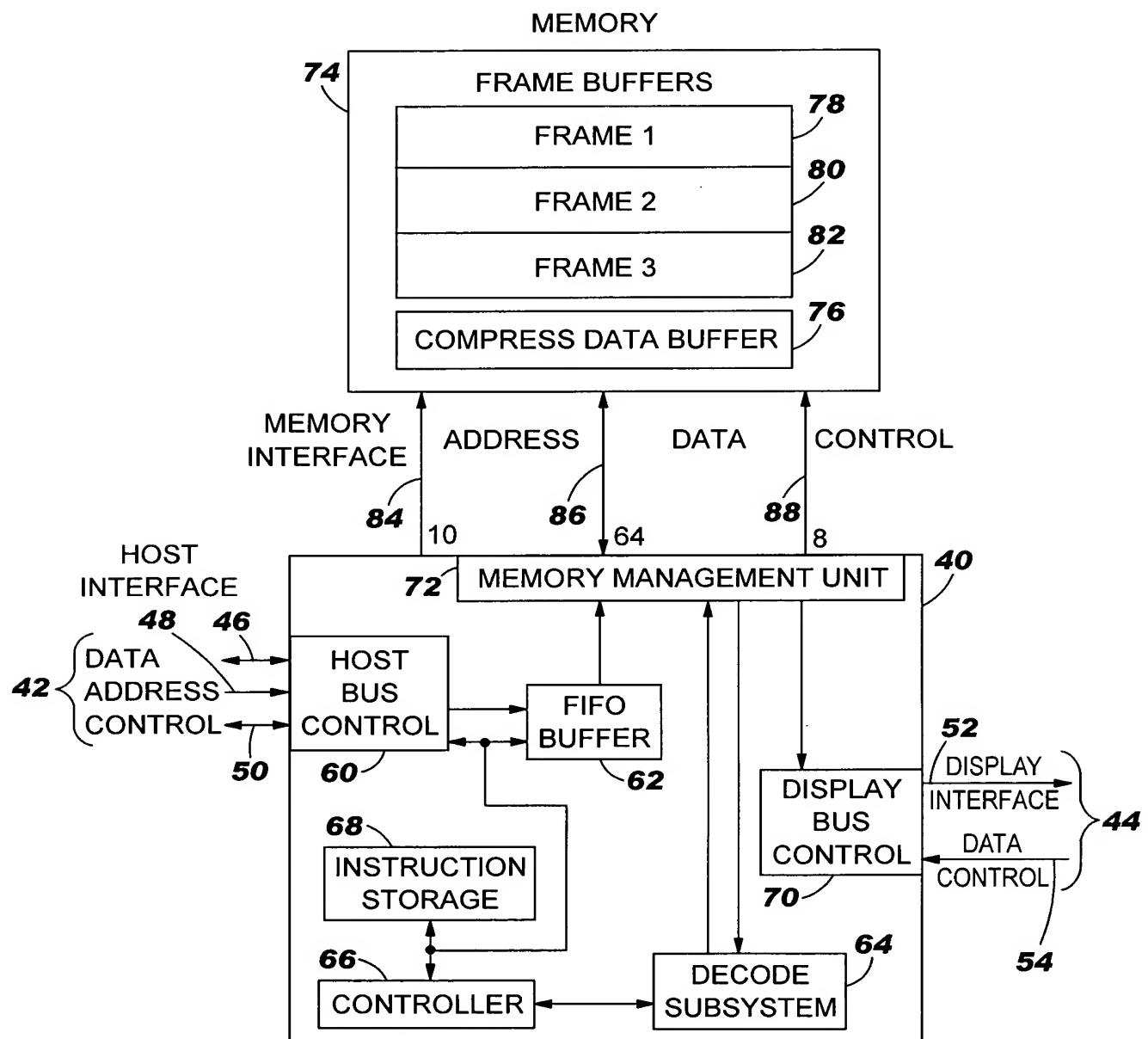


FIG. 4

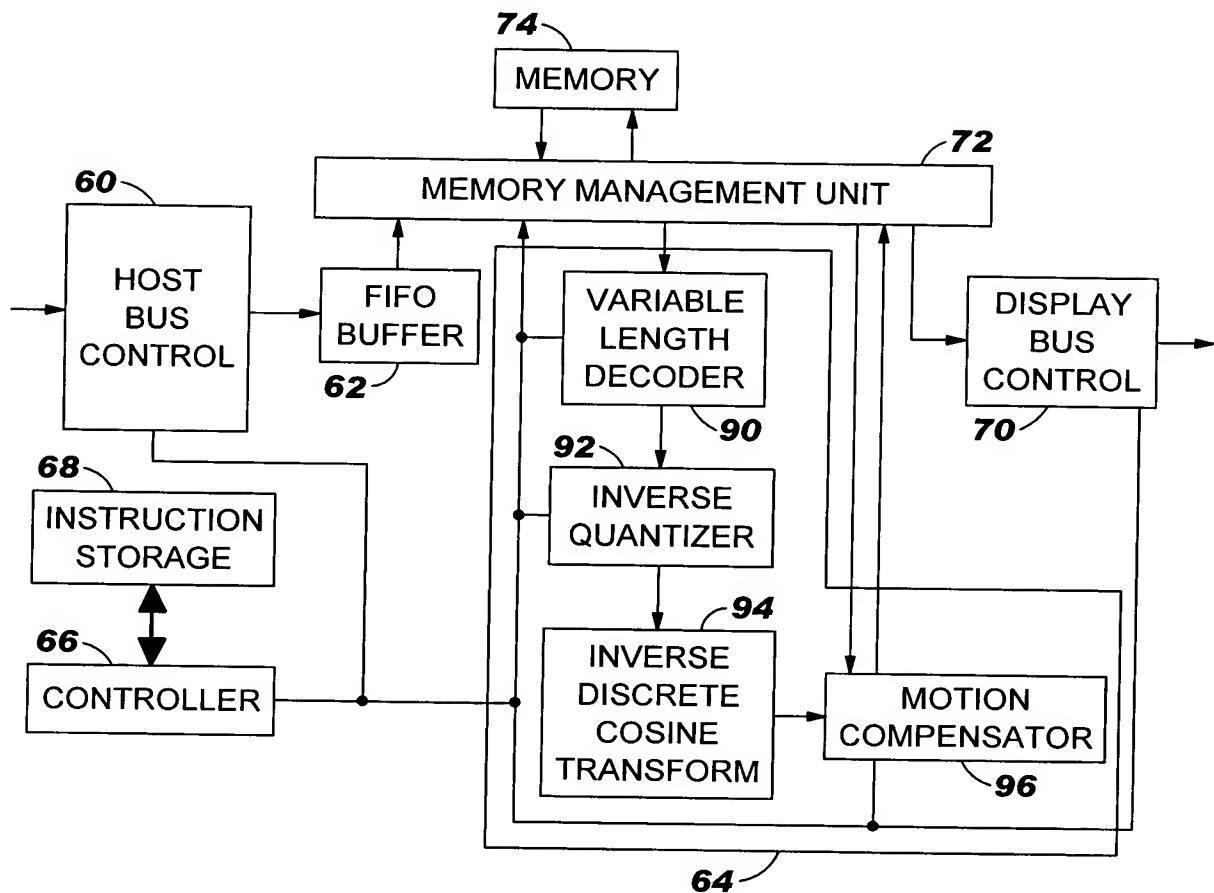


FIG. 5

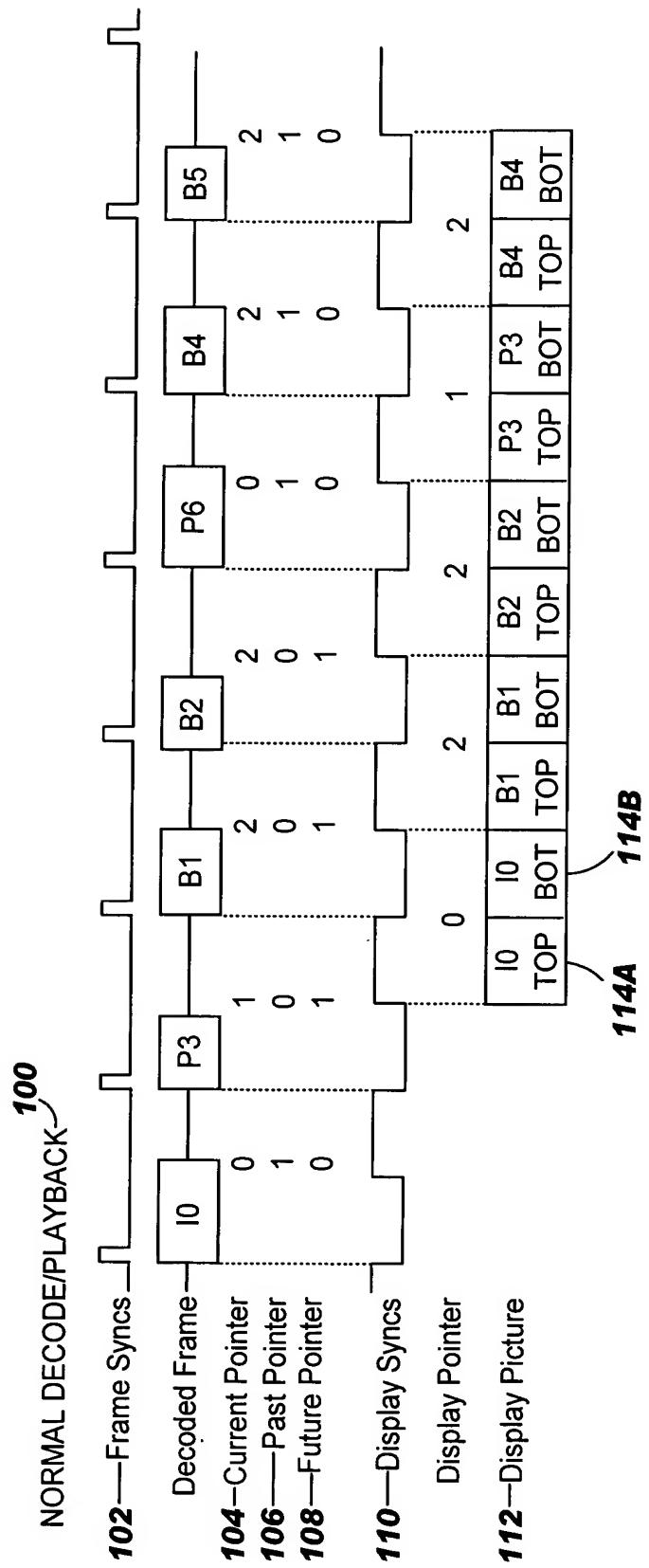
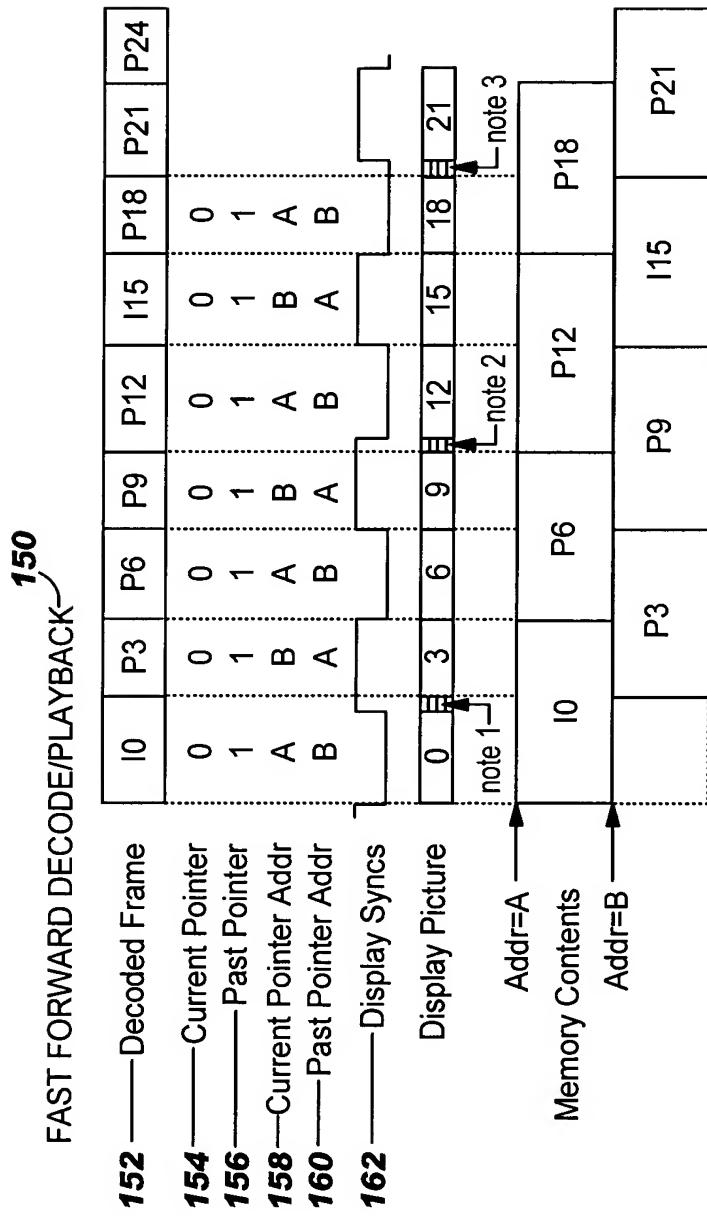


FIG. 6



Note 1 - Top portion is formed from picture 0 since picture 3 has not decoded yet

Note 2 - Bottom portion is formed by picture 6 since picture 12 has not decoded far enough yet

Note 3 - Bottom portion is taken from picture 15 since picture 21 has not decoded far enough yet

FIG. 7

		NORMAL MODE (HEX ADR)	LOW DELAY MODE (HEX ADR)
USER ORG	<b>200</b>	0	0
OSD ORG	USER DATA BUFFER	200	200
<b>206</b> -Y0 ORG	OSD BUFFER	BA00	BA00
<b>202</b>	BUFFER 0 LUMA	60000	60000
UV0 ORG	BUFFER 0 CHROMA	8A300	BA00
<b>208</b> -Y1 ORG	BUFFER 1 LUMA	DE900	60000
<b>204</b>	BUFFER 1 CHROMA	108C00	BA00
UV1 ORG	BUFFER 2 LUMA	15D200	60000
Y2	BUFFER 2 CHROMA	187500	BA00
UV2 ORG	SPILL BUFFER LUMA	192900	B7300
Y-SPILL ORG	SPILL BUFFER CHROMA	198300	CDB00
UV-SPILL ORG	COMPRESS DATA BUFFER		
DATA ORG			